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paperback. Book Condition: New. Ship out in 2 business day, And Fast shipping, Free Tracking number will be provided after the shipment. Paperback. Publisher: Harbin Institute of Technology Press Pub. Date :2006-09-01. The book about the TI 16-bit fixed-point of U.S. companies TMS320LF240x series DSP chip and its application system design. TMS320LF240x series chip is currently the world's most integrated, highest performance and is widely used in automatic control, smart instrumentation, mechatronics, industrial automation and other fields of DSP chip. This book introduces the hardware structure TMS320LF240x chips, each chip peripheral components, applications, hardware design, simulation development system to use, and how to use C language, C language and assembly language to write applications. The materials available for automatic control, smart instrumentation, mechatronics, industrial automation professional master's graduate, undergraduate use, is also available in the vast field of engineering and technical officers. Contents: Chapter 1 math signal processor (DSP) Overview 1.1 What is DSP 1.2 DSP technology development and application of the status quo 1.3 DSP 1.4 DSP and microcontroller, embedded microprocessor difference 1.5 DSP's basic structure and main features of the 1.6 DSP's 1.7 Classification and major technical indicators to choose DSP systems DSP TMS320LF240x Chapter 2 Overview 2.1 TI's TMS320 series DSP DSP Series Overview 2.2 TMS320LF240x Introduction Chapter 3 TMS320LF240x the CPU modules and clock modules 3.2 Module 3.1 CPU PLL (PLL) clock module and low-power mode Chapter 4 System Configuration and System Configuration Register interrupt module 4.1 4.2 Interrupt Priority and Interrupt Vector 4.4 4.3 peripheral interrupt expansion controller interrupts the process interrupt response latency 4.5 4.6 CPU Interrupt Registers 4.7 Reset and invalid address detection interrupt control register 4.9 4.8 External maskable interrupt routines implement Chapter 5, crying and stored within the memory IO space 5.1 5.3 5.2 Program Memory...



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